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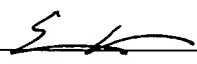
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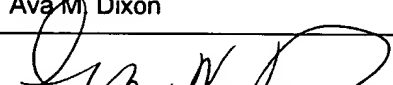
PTO/SB/21 (08-00)

U.S. PATENT & TRADEMARK OFFICE
JUL 30 2002

TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application Number	09/583,087
	Filing Date	September 5, 2000
	First Named Inventor	Shunpei YAMAZAKI
	Group Art Unit	2815
	Examiner Name	J. Jackson, Jr.
Total Number of Pages in This Submission		Attorney Docket Number 0756-2203

ENCLOSURES (check all that apply)		
<input checked="" type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment / Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Assignment Papers (for an Application) <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Declaration and Power of Attorney <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s)	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosures 1. Preliminary Amendment 2. 3. 4. 5. 6.
Remarks <input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge any additional fees required or credit any overpayments to Deposit Account No. 50-2280 for the above identified docket number.		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Eric J. Robinson, Reg. No. 38,285 Robinson Intellectual Property Law Office, P.C. PMB 955 21010 Southbank Street Potomac Falls, VA 20165
Signature	
Date	July 22, 2002

CERTIFICATE OF MAILING			
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on the date indicated below.			
Type or printed name	Ava M Dixon		
Signature		Date	July 22, 2002

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Washington, DC 20231.**

**FEE TRANSMITTAL
FOR FY 2001**

Patent fees are subject to annual revision.

TOTAL AMOUNT OF PAYMENT (\$) 0.00**Complete if Known**

Application Number	09/583,087
Filing Date	September 5, 2000
First Named Inventor	Shunpei YAMAZAKI
Examiner Name	J. Jackson, Jr.
Group Art Unit	2815
Attorney Docket No.	0756-2203

METHOD OF PAYMENT

1. ☐ The Commissioner is hereby authorized to charge indicated fees and credit any overpayments to:

Deposit
Account
Number 50-2280Deposit
Account
Name Robinson Intellectual Property
Law Office

- ☒ Charge Any Additional Fee Required
Under 37 CFR 1.16 and 1.17 and
credit any overpayment

- ☐ Applicant claims small entity status.
See 37 CFR 1.27

2. ☒ Payment Enclosed:

- ☒ Check ☐ Credit Card ☐ Money
Order ☐ Other

FEE CALCULATION**1. BASIC FILING FEE**

Large Entity Fee Code	Fee (\$)	Small Entity Fee Code	Fee (\$)	Fee Description	Fee Paid
101	740	201	370	Utility filing fee	
106	330	206	165	Design filing fee	
107	510	207	255	Plant filing fee	
108	740	208	370	Reissue filing fee	
114	160	214	80	Provisional filing fee	

SUBTOTAL (1) (\$)**2. EXTRA CLAIM FEES**

Total Claims	Extra Claims	Fee from below	Fee Paid
173	-182**	0 X 0.00	= 0.00

Independent Claims	Extra Claims	Fee from below	Fee Paid
13	-14**	0 X 0.00	= 0.00

Multiple Dependent	Fee from below	Fee Paid

Large Entity Fee Code	Fee (\$)	Small Entity Fee Code	Fee (\$)	Fee Description
103	18	203	9	Claims in excess of 20
102	84	202	42	Independent claims in excess of 3
104	280	204	140	Multiple dependent claim, if not paid
109	84	209	42	** Reissue independent claims over original patent
110	18	210	9	** Reissue claims in excess of 20 and over original patent

SUBTOTAL (2) (\$) 0.00

**or number previously paid, if greater; For Reissues, see above

FEE CALCULATION (continued)**3. ADDITIONAL FEES**

Fee Code	Large Entity Fee (\$)	Fee Code	Small Entity Fee (\$)	Fee Description	Fee Paid
105	130	205	65	Surcharge - late filing fee or oath	
127	50	227	25	Surcharge - late provisional filing fee or cover sheet	
139	130	139	130	Non-English transaction	
147	2,520	147	2,520	For filing a request for <i>ex parte</i> reexamination	
112	920*	112	920*	Requesting publication of SIR prior to Examiner action	
113	1,840*	113	1,840*	Requesting publication of SIR after Examiner action	
115	110	215	55	Extension for reply within first month	
116	400	200	200	Extension for reply within second month	
117	920	460	460	Extension for reply within third month	
118	1,440	720	720	Extension for reply within fourth month	
128	1,960	228	980	Extension for reply within fifth month	
119	320	219	160	Notice of Appeal	
120	320	220	160	Filing a brief in support of an appeal	
121	280	221	140	Request for oral hearing	
138	1,510	138	1,510	Petition to institute a public use proceeding	
140	110	240	55	Petition to revive - unavoidable	
141	1,280	241	640	Petition to revive - unintentional	
142	1,280	242	640	Utility issue fee (or reissue)	
143	460	243	230	Design issue fee	
144	620	244	310	Plant issue fee	
122	130	122	130	Petitions to the Commissioner	
123	50	123	50	Processing fee under 37 CR 1.17(q)	
126	180	126	180	Submission of Information Disclosure Stmt	
581	40	581	40	Recording each patent assignment per property (times number of properties)	
146	740	246	370	Filing a submission after final rejection (37 CFR § 1.129(a))	
149	740	249	370	For each additional invention to be examined (37 CFR § 1.29(b))	
179	740	279	370	Request for Continued Examination (RCE)	
169	900	169	900	Request for expedited examination of a design application	

Other fee (specify) _____

* Reduced by Basic Filing Fee Paid

SUBTOTAL (3) (\$)**CERTIFICATE OF MAILING**

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Name: Ava M. Dixon

SUBMITTED BY

Name (Print/Type) Eric J. Robinson

Registration No.
(Attorney/Agent)

38,285

Complete (if applicable)

Telephone (571) 434-6789

Signature

Date

June 21, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)
Shunpei YAMAZAKI)
Serial No. 09/583,087)
Filed: September 5, 2000)
For: ELECTRO-OPTICAL DEVICE)
AND METHOD FOR)
MAUFACTURING THE SAME)



Art Unit: 2815

Examiner: J. Jackson, Jr.

CERTIFICATE OF MAILING

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Washington, D.C. 20231, on 7/30/02

PRELIMINARY AMENDMENT

Honorable Commissioner of Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated February 21, 2002 and further to the
Request for Continued Examination filed June 21, 2002, please amend the above-
identified application as follows:

IN THE CLAIMS:

Please amend claims 21-41, 43-44, 48, 53, 57-59, 64 and 66 as follows:

21. (Twice Amended) An active matrix display device having a pixel portion
and a driver circuit portion, said driver circuit portion including at least one pair of
complementary p-channel and n-channel thin film transistors, and said pixel portion
including at least one thin film transistor, each of said transistors comprising:

a crystalline semiconductor island on an insulating surface, said semiconductor
island having source and drain regions and a channel region;

a gate insulating film adjacent to at least said channel region;

a gate electrode adjacent to said gate insulating film;

a leveling film covering each of said thin film transistors in both of the pixel
portion and a part of the driver circuit portion;